



PTO/SB/81 (04-05)

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INDICATION FORM**

Application Number	10/672,125
Filing Date	September 26, 2003
First Named Inventor	Dillon, Michael N.
Title	Semiconductor Integrated Circuit...
Art Unit	2838
Examiner Name	Nguyen, Matthew Van
Attorney Docket Number	03-0833

I hereby revoke all previous powers of attorney given in the above-identified application.

I hereby appoint:

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024319

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☐ Applicant/Inventor.☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)**SIGNATURE of Applicant or Assignee of Record**

Signature	<i>Timothy R. Croll</i>	Date	<i>July 4 05</i>
Name	Timothy R. Croll	Telephone	(408) 433-7625
Title and Company	Senior Corporate Counsel, IP, LSI Logic Corporation		

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

☒ *Total of 1 forms are submitted.

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: LSI Logic CorporationApplication No./Patent No.: 10/672,125 Filed/Issue Date: September 26, 2003Entitled: Semiconductor Integrated Circuit Having Voltage-Down Circuit Regulator and Charge SharingLSI Logic Corporation, a Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
The extent (by percentage) of its ownership interest is _____ %

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 014555, Frame 0282, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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☐ Additional documents in the chain of title are listed on a supplemental sheet.☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Signature

Timothy R. Croll

Printed or Typed Name

Senior Corporate Counsel, IP

Title

7 JULY 05

Date

(408) 433-7625

Telephone Number

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